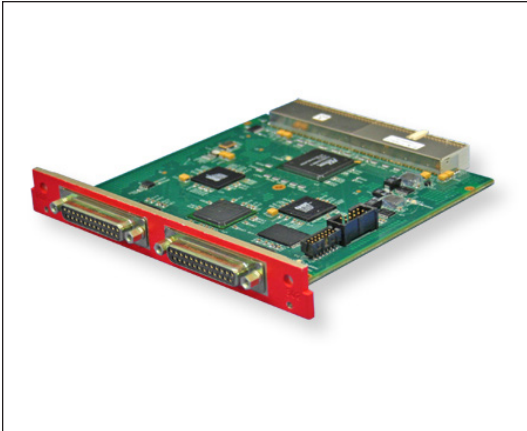


Dual-Channel LVDS Radar Receiver Board



Applications

- Distributed data acquisition systems
- CAIS applications
- Radar data collection for test data recording and analysis

Features

- Low-Voltage Differential Signaling (LVDS) receiver board for the AIM-4004-1
- Up to two RDR-302-1 boards can be installed in each chassis
- Interfaces with the AIM-4004-1 chassis over a modified CompactPCI® bus backplane
- Two combined channels can receive data at up to a maximum aggregate rate of 120 MBps
- RDR-302-1 terminates messages, buffers and timestamps inbound data

Description

LVDS provides signal integrity, low jitter and good common-mode performance. The RDR-302-1 is an LVDS receiver board for the AIM-4004-1 product line that has two LVDS receive channels that together support a maximum aggregate data rate of 120 MBps. It employs an FPGA plus dual-port SRAM to terminate messages, buffer inbound data and provide data timestamping.

Revision 05/13/2015

RDR-302-1 Datasheet

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Specifications subject to change without notice.

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Teletronics - A Curtiss-Wright Company

15 Terry Drive, Newtown, PA 18940

phone: 267.352.2020 fax: 267.352.2021 Sales@ttcdas.com

www.ttcdas.com



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