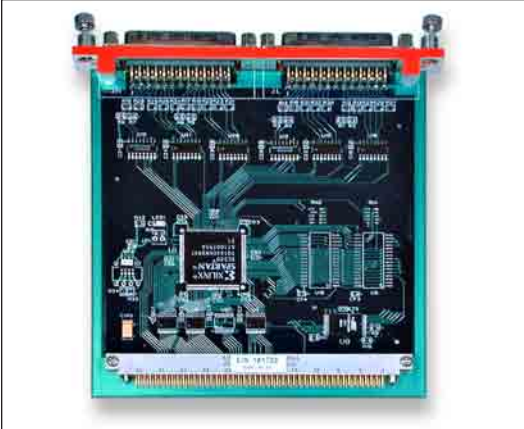


Built-in Test and Transmitter Programming Card



Applications

- Data acquisition and monitoring
- System health monitoring
- Built-in test

Features

- Provides 10 programmable frequency select output bits for transmitter setup
- Transmitter frequency select bits are driven by optocouplers
- Select up to 64 PCM parameters for test
- PCM parameters can physically be located anywhere within the distributed system
- 8 flag outputs for visually viewing errors are driven with RS-422 differential drivers
- 4 of the 8 flags (flag 1-4) also driven by optocouplers
- Any parameter error condition can be viewed at any or all of the 8 flag outputs (programmable)
- Flag duration is programmable
- Test results of all 64 parameters can be inserted into the PCM
- Used in a master EDAU/CDAU-20XX
- Microsoft Windows application software included

Description

The BIT-100 provides limit comparison and absolute value testing of up to 64 PCM parameters. It also provides 10 output bits for transmitter setup. Testing includes two's complement data. The failure of any parameter can be monitored at any or all of the 8 programmable flag outputs. Flags 1-4 are also driven out using opto-isolators. Each parameter result is uniquely identified in a 64-bit error register. This error register is translated into error status words that can be inserted into the PCM. At 16 BPW, the results of all 64 parameters can be read in 4 reads (4x16 = 64 parameters). Extended reads are provided when operating at less than 16 BPW.

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BIT-100 Datasheet

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