

Single Channel PCM Merger Board



Applications

- Integrate PCM encoders from different vendors
- Integrate low rate and high rate systems

Features

- Single channel PCM merger board
- Accepts IRIG 106-96 serial data streams
- FIFO based architecture/RAM base architecture
- Encodes data as an asynchronous embedded format
- Uses empty flags to support data recovery (when operating in FIFO mode)
- Accepts NRZ-L/RNRZ-L data and bit clock
- RS-422 or TTL Inputs
- Compatible with EDAU/CDAU-20XX product families
- Microsoft Windows application software included

Description

The DCM-101 is a single channel PCM merger board for use in TTC's EDAU-20XX or CDAU-20XX series products. The board can be used to merge PCM inputs from different vendors with TTC's EDAU/CDAU product lines. The board accepts either single-ended or RS-422 differential inputs (data and clock) at standard rates up to 2 Mbps. All incoming data is FIFO buffered or RAM buffered and is selectable by the user for transmission in the PCM output format as "Class I PCM" (standard fixed position). The embedded PCM data's format is not synchronized with the EDAU/CDAU PCM format.

Revision 10/15/2010

DCM-101 Datasheet

©2010 Teletronics Technology Corporation
Specifications subject to change without notice.



Teletronics Technology Corporation
15 Terry Drive, Newtown, PA 18940
phone: 267.352.2020 fax: 267.352.2021 Sales@ttcdas.com

www.ttcdas.com