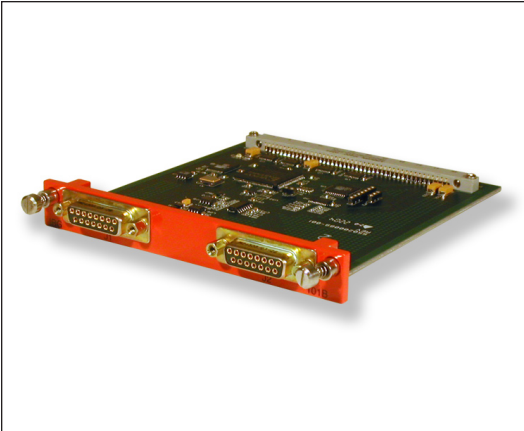


## IRIG Time Reader/Generator Card



### Applications

- Flight Test Instrumentation
- Wideband Testing, Structural Analysis, ...
- Physical Research and Experimentation

### Features

- Time Code Reader/Generator card
- IRIG-B Format (Others available as an option)
- Accepts Modulated (AC) or Unmodulated (DC) inputs
- Generates IRIG-B DC output
- Use for Frame Time Tagging, Bus Word Time Tagging
- Generates Status Words for Transmission in PCM
- Compatible with WDAU-2000 systems
- "Flywheel Mode" (Runs on internal oscillator)
- Mission "Elapsed Time" Mode (External Reset)
- External battery backup provisions
- Time compatible with IRIG-106-96
- Compatible with WDAU-20XX operating to 20Mbps
- Windows 95/98/NT/2000 Software included

### Description

The IRG-101B card provides IRIG-B time code capability to the WDAU-2000 CAIS Bus Data Acquisition Unit. The card can be placed in any I/O slot. The card accepts an external IRIG-B, AC or DC time source for frame time tagging purposes, and in addition, provides per-word or per-message time tagging capability for certain I/O cards in the hot chassis that acquire bus data (e.g. 1553, ARINC-429, RS-232, etc.). The IRIG-101B card also provides an IRIG-B DC time code output for cascading purposes. All encoded time words furnished by the IRIG-101B card comply with IRIG-106-96, Chapter 4 requirements for "high time", "low time" and "micro time" formats. The card also has a built-in microprocessor and ADC to measure and encode the internal WDAU-2000 system temperature and internal voltage rails within the host WDAU-2000 encoder.

Revision 03/13/2009

### IRG-101B Datasheet

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