

IRIG Time Reader/Generator Module



Applications

- Flight Test Instrumentation
- Wideband Testing, Structural Analysis
- Physical Research and Experimentation

Features

- Time Code Reader/Generator Module
- IRIG-B Format (Others Available as an Option)
- Accepts Modulated (AC) or Unmodulated (DC) Inputs
- Generates IRIG-B DC Output
- Use for Frame Time Tagging, Bus Word Time Tagging
- Generates Status Words for Transmission in PCM
- Compatible with MEDAU/MCDAU-2000 System
- “Flywheel Mode” (Runs on Internal Oscillator)
- Mission “Elapsed Time” Mode (External Reset)
- External Battery Backup Provisions
- Time Compatible with IRIG-106-96
- Windows 95/98/NT/2000 Software Included

Description

The MIRG-101B module provides IRIG-B time code capability to the MEDAU-2000 Miniature E-Bus Data Acquisition Unit or the MCDAU-2000 Miniature CAIS Bus Data Acquisition Unit. The module can be placed in either a master or remote unit and will provide time code capability for the modules within that particular stack. The module accepts an external IRIG-B, AC or DC time source for frame time tagging purposes, and in addition, provides per-word or per-message time tagging capability for certain I/O modules in the stack that acquire bus data (e.g. 1553, ARINC-429, RS-232, etc.). The MIRG-101B module also provides an IRIG-B DC time code output for cascading purposes. All encoded time words furnished by the MIRG-101B module comply with IRIG-106-96, Chapter 4 requirements for “high time”, “low time”, and “micro time” formats. The module also has a built-in microprocessor and ADC to measure and encode the internal MEDAU-2000 system temperature and internal voltage rails within the host MEDAU-2000 Encoder.

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MIRG-101B Datasheet

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